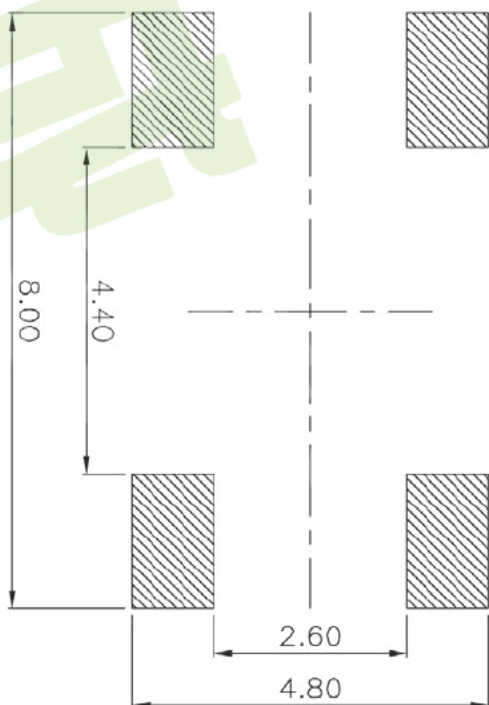


Circuit Diagram



PCB Layout (Pattern side)

NO.1		
PART NO	QTY	REMARK
1	90±30	vigor
2	130±30	
3	180±30	
4	250±30	
5	350±50	

NO.2-NO.3		
PART NO	NOTE	SERIAL NUMBER
01~99		

ITEM	PART NAME	TER'NO.	QTY.	MATERIAL	FINISHING	REMARK
④	Dust Cover	—	1	Kapton Film	Yellow	—
③	Terminal	—	4	Phosphor Bronze	Plating silver	—
②	Contact	—	1	F.Ag/SUS	Natural	—
①	Base	—	1	Nylon	Black	—

APPROVALS		DATE
DRAWN	JKUN.LU	2013/8/29

APPROVALS		CHECKED		APPROVALS		CHECKED		APPROVALS		CHECKED													
ECN NO.	REV.	DATE.	DESCRIPTION.	CHANGE.	CHECK.	APPRO.	NEW																
	A																						
TOLERANCES ARE: <table border="1" style="display: inline-table; margin-left: 10px;"> <tr> <td>30~0.15</td> <td>±0.30</td> <td>ANGLE</td> </tr> <tr> <td>10~30</td> <td>±0.20</td> <td></td> </tr> <tr> <td>5~10</td> <td>±0.15</td> <td></td> </tr> <tr> <td>~5</td> <td>±0.10</td> <td></td> </tr> </table>												30~0.15	±0.30	ANGLE	10~30	±0.20		5~10	±0.15		~5	±0.10	
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~5	±0.10																						
DRAWN			DATE			WEBSITE			PART NO.														
JKUN.LU			2013/8/29			www.ycelectron.com			M2-1818P														
CHECKED			DATE			SCALE: 1:1			PROJ. SHEET 1 OF 1														

深圳市裕诚电子有限公司
 SHENZHEN YOCENG ELECTRONICS CO., LTD