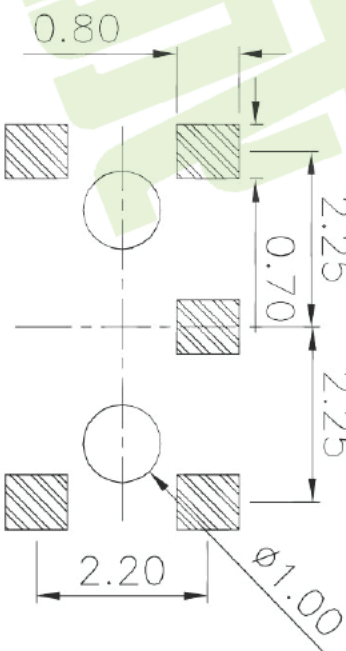


Circuit Diagram



PCB Layout(Pattern side)

- Note:
1. Rating : DC 12V 50mA
 2. Contact Resistance : $\leq 50m\Omega$
 3. Insulation Resistance: DC500V 1Minute
 4. Operating Temperature Range: $-25^{\circ}C+80^{\circ}C$
 5. Ambient Humidity Used: $\leq 85\%RH$
 6. Operating Force : $150\pm 80gf$
 7. Operating Life : 50,000Cycles

| ITEM | PART NAME | TER'NO. | QTY. | MATERIAL | FINISHING | REMARK |
|------|----------------|---------|------|-----------------|-----------|--------|
| ⑤ | Contact Spring | — | 1 | Beryloy | Ag Plated | — |
| ④ | Terminal | — | 5 | Phosphor Bronze | Ag Plated | — |
| ③ | Base | — | 1 | LCP | Black | — |
| ② | Cover | — | 1 | SUS | Natural | — |
| ① | KNOB | — | 1 | PA46 | RED | — |

| APPROVALS | DATE |
|---------------|-----------|
| DRAWN JKUN,LU | 2013/10/8 |

深圳市裕诚电子有限公司
 SHENZHEN YUCHENG ELECTRONICS CO., LTD

| CHECKED | WEBSITE |
|-----------|--|
| APPROVALS | www.ycelectron.com |

| APPROVALS | PART NO. | SCALE: | PROJ: |
|----------------|----------|-------------|--------------|
| TOLERANCES ARE | M3-25865 | 1:1 | PROJ: |
| 30±0.15 | | | |
| 10~30 ±0.20 | | | |
| 5~10 ±0.15 | | | |
| ~5 ±0.10 | | | |
| | | ±2' | |
| | | DRAWING NO. | |
| | | | SHEET 1 OF 1 |

| ECN NO. | REV. | DATE. | DESCRIPTION. | CHANGE. | CHECK. | APPRO. |
|---------|------|-------|--------------|---------|--------|--------|
| | A | | NEW | | | |